

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

|             |                        |   |                   |              |
|-------------|------------------------|---|-------------------|--------------|
| Applicants: | Nicholas HOLIAN et al. | § | Confirmation No.: | 7674         |
|             |                        | § |                   |              |
| Serial No.: | 10/763,693             | § | Group Art Unit:   | 2113         |
|             |                        | § |                   |              |
| Filed:      | 01/23/2004             | § | Examiner:         | Y. L. Wilson |
|             |                        | § |                   |              |
| For:        | Error Simulation For A | § | Docket No.:       | 200312299-1  |
|             | Memory Module          | § |                   |              |

**RESPONSE TO OFFICE ACTION DATED AUGUST 22, 2006**

Mail Stop Amendment  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Date: December 21, 2006

**AMENDMENT**

Sir:

In response to the Office Action of August 22, 2006, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims that begins on page 2 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.